

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Johannes Becker
Serial No.: 10/706,365
Filed: November 12, 2003
Title: SYSTEM AND METHOD FOR SECURING AN INTEGRATED CIRCUIT
AS AGAINST SUBSEQUENT REPROGRAMMING
Grp./A.U.: 2135
Examiner: Suman Debnath Confirmation No.: 6816

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed
with United States Patent and trademark Office on:

June 4, 2007 (Date)

Debbie Sams

(Printed or typed name of person signing the certificate)

/Debbie Sams/

(Signature of the person signing the certificate)

Sir:

REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.111

The Applicant has carefully considered this application in connection with the non-final Examiner's Action electronically delivered March 2, 2007, and respectfully requests reconsideration of this application in view of the following remarks.